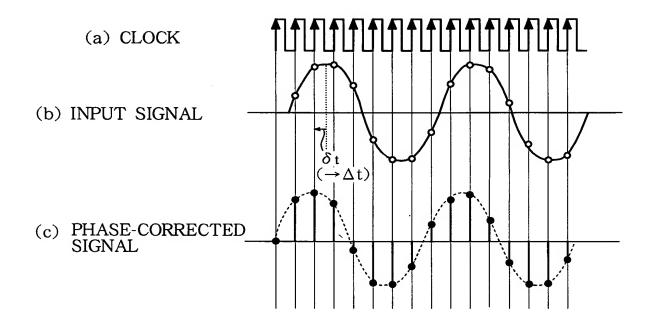


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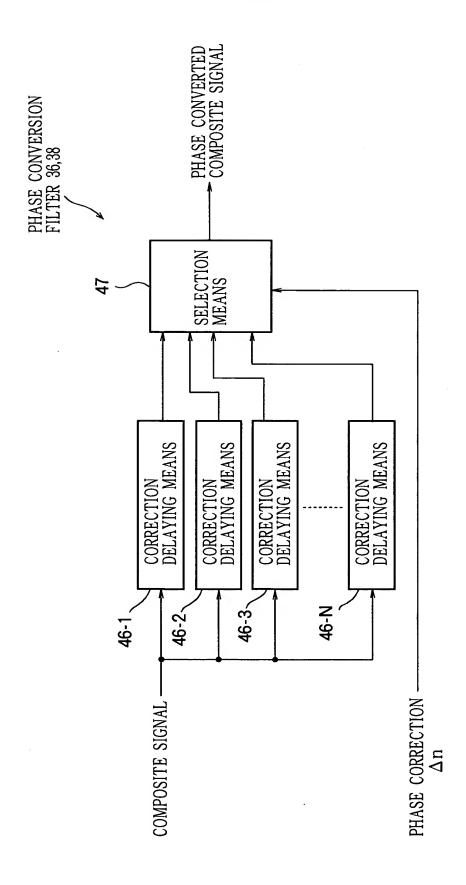
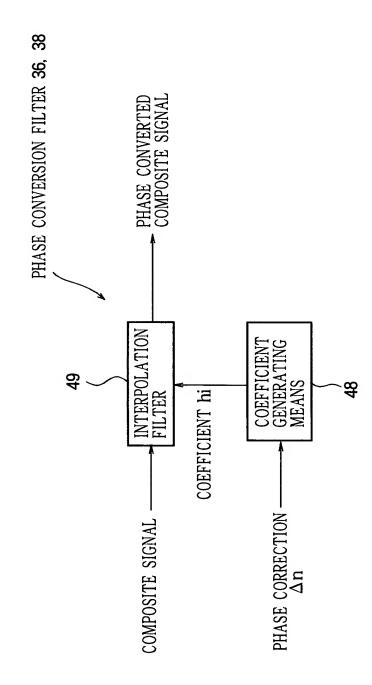
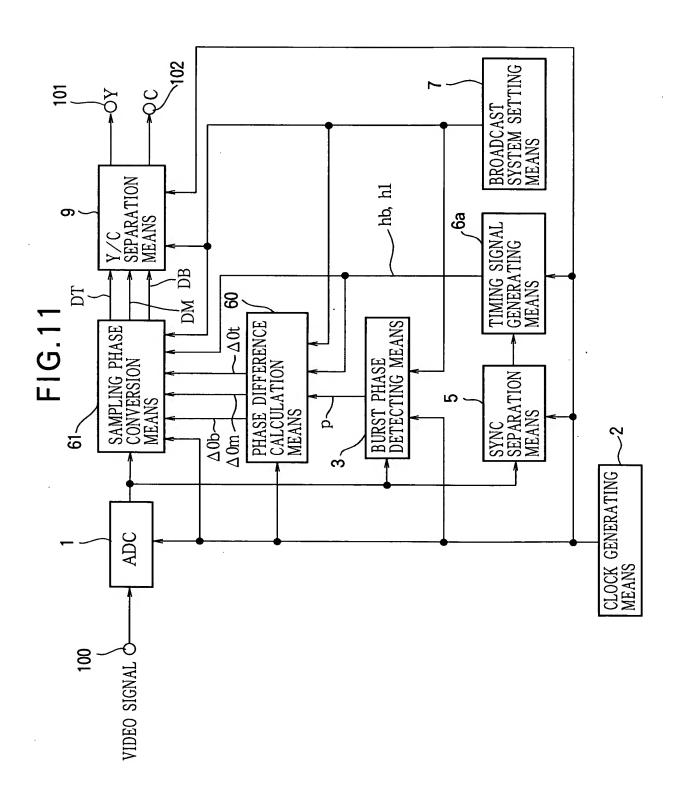


FIG.9







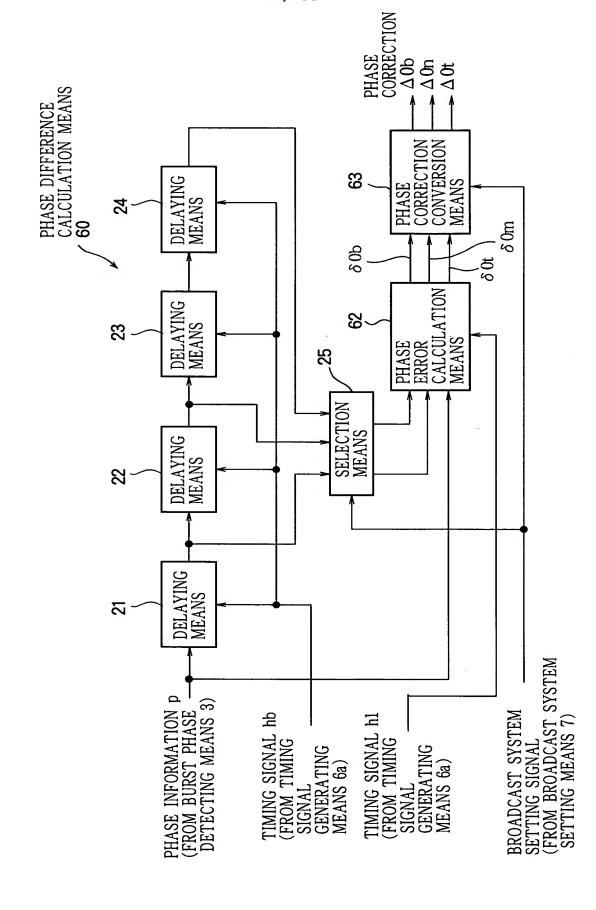
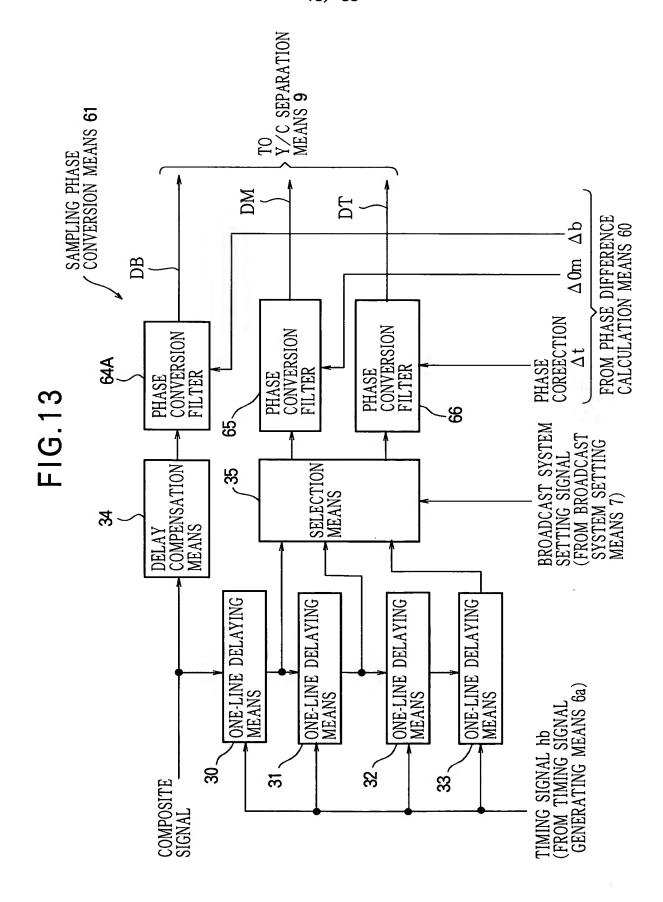
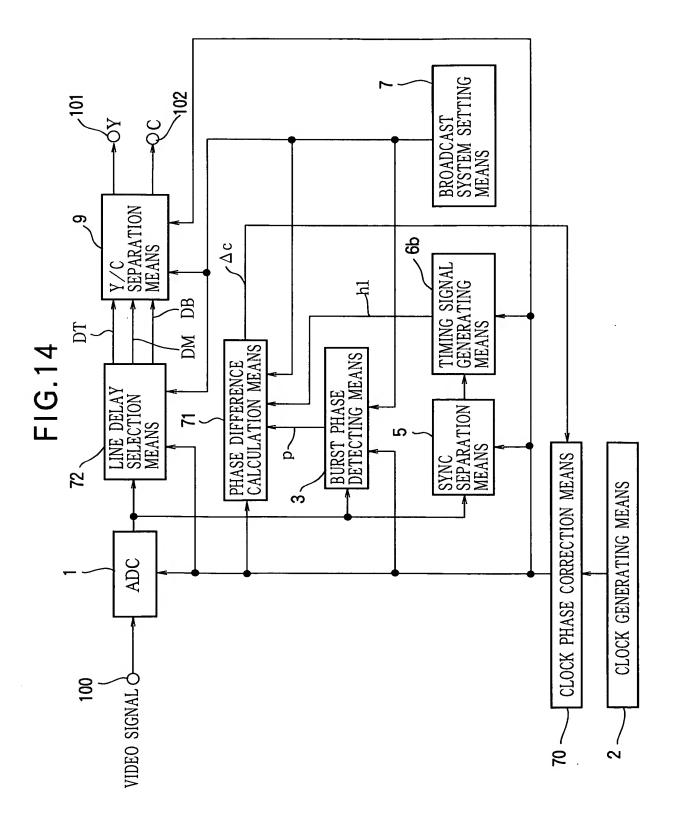
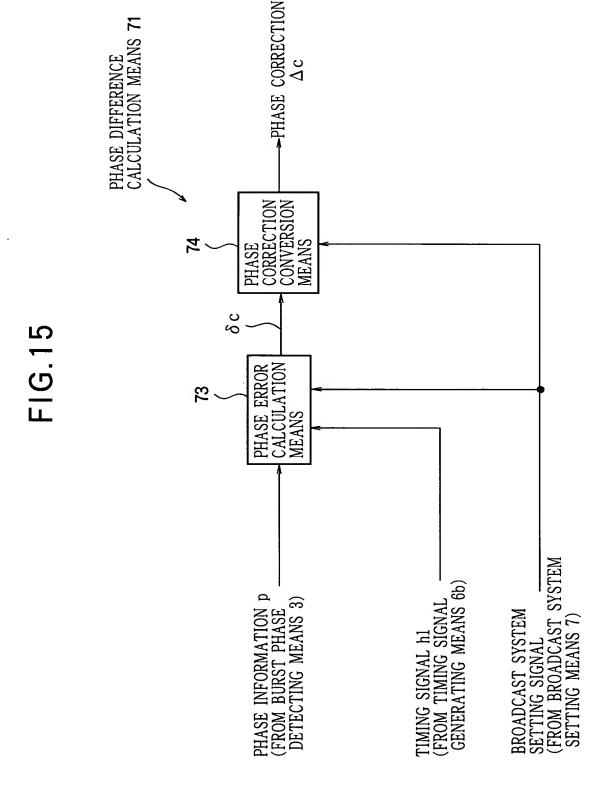


FIG.12





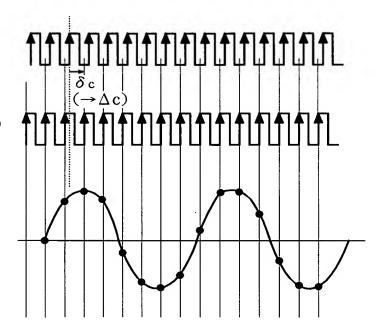


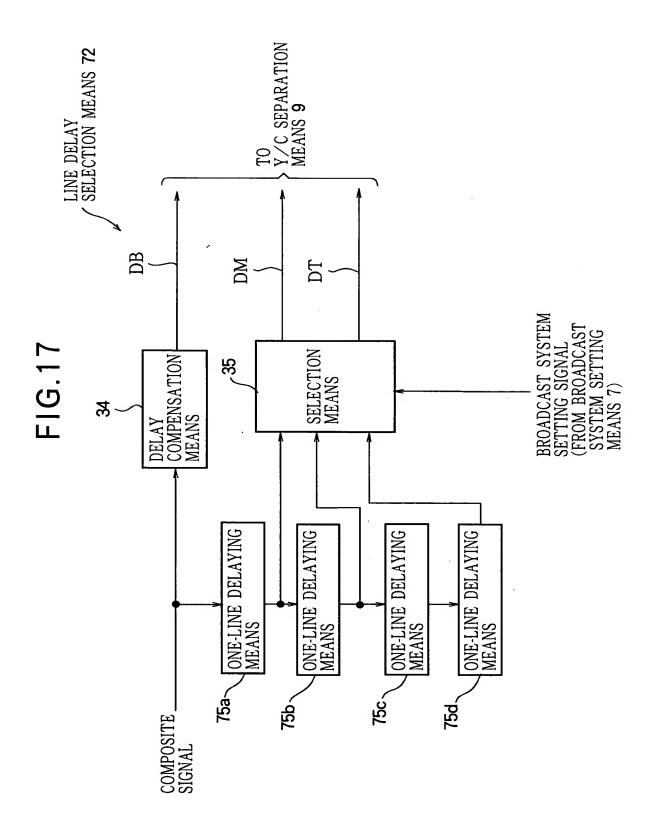
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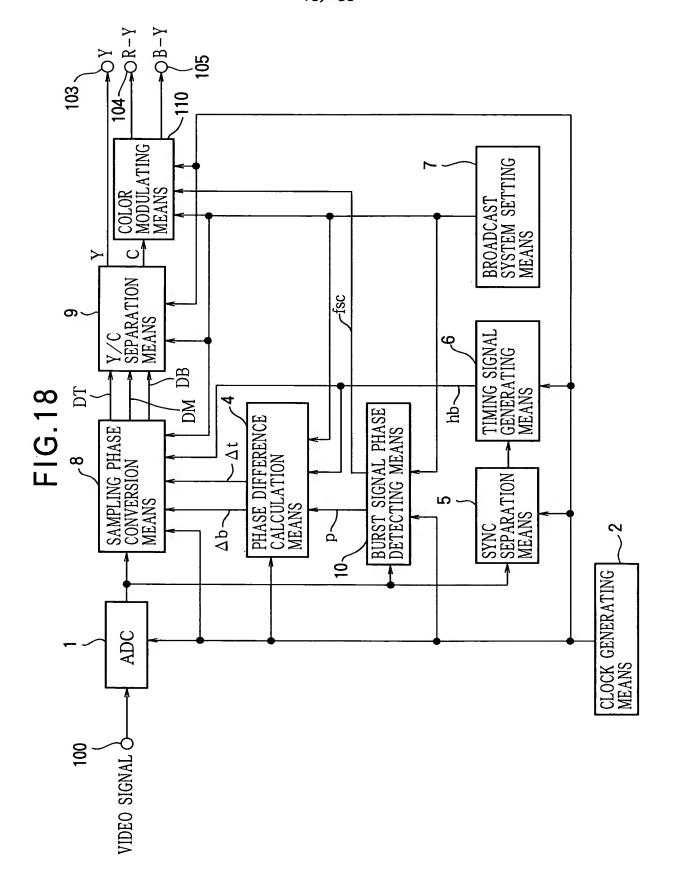
(a) CLOCK

(b) PHASE-CORRECTED CLOCK

(C) INPUT SIGNAL







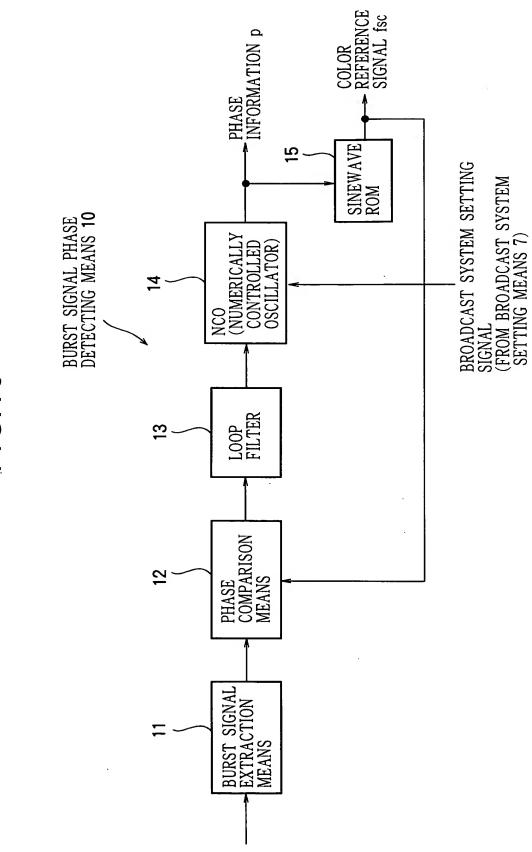
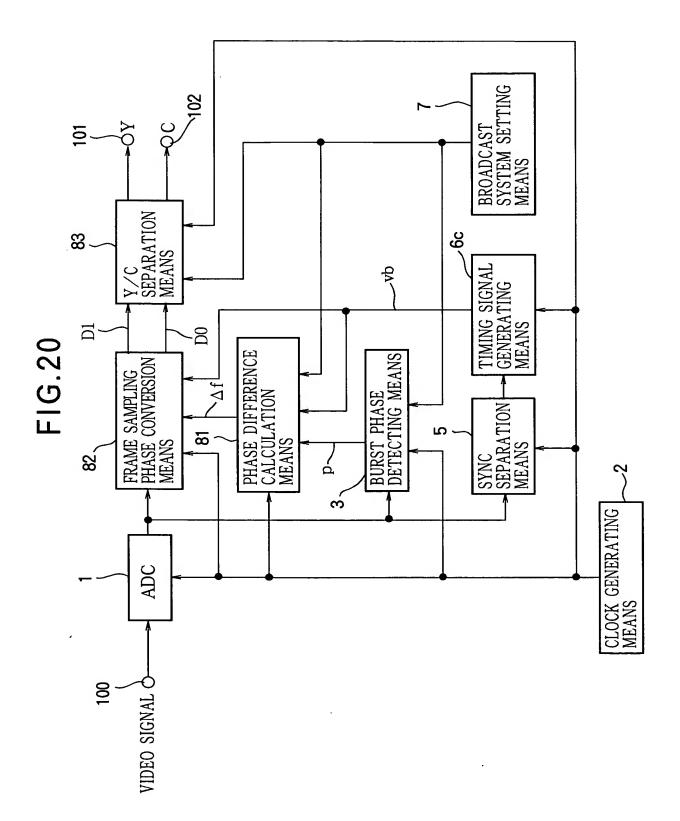


FIG.19



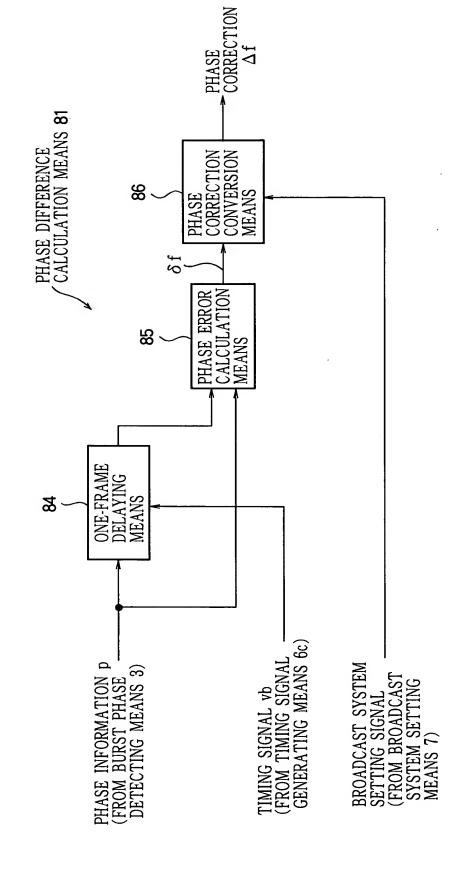
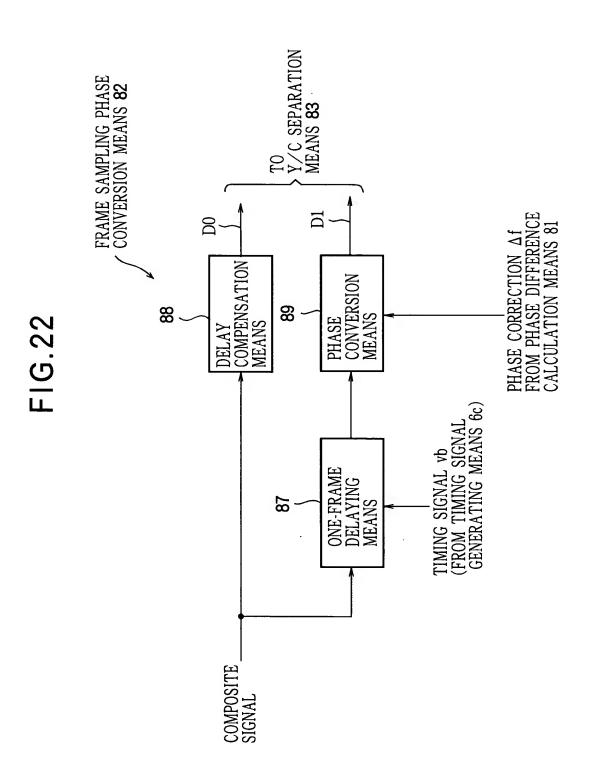
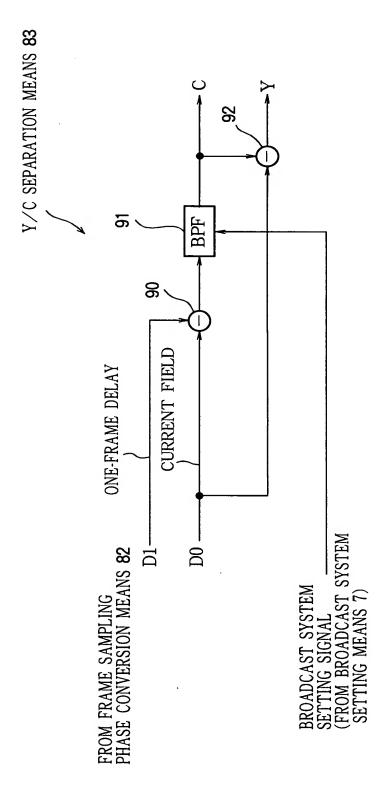


FIG.21







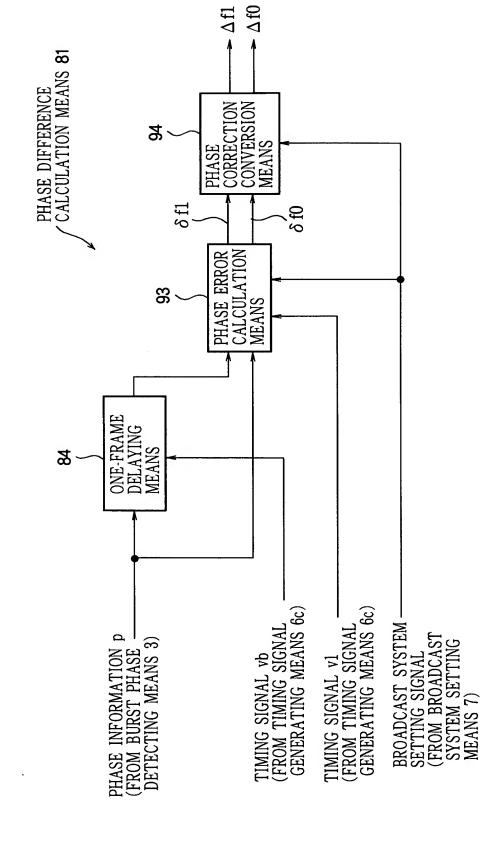


FIG.24

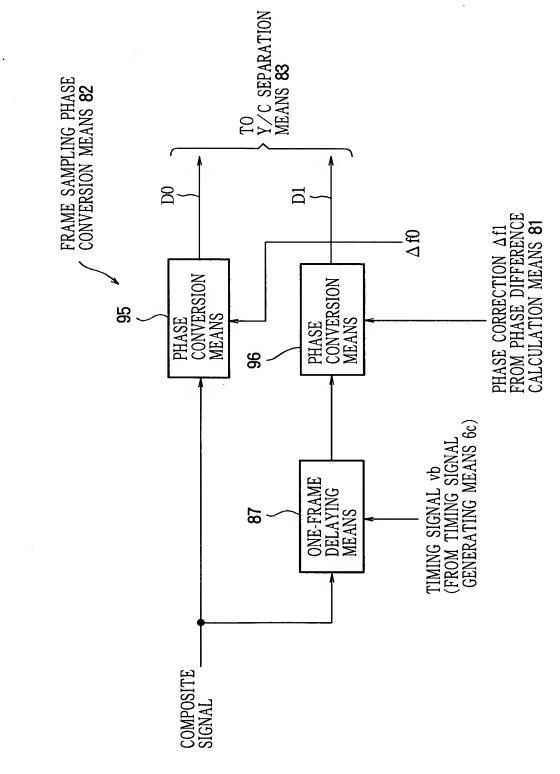


FIG.25

